2-byte addition with carry.
Relative addressing of data in P2: P2 = 0F00
input : **0F** => **0FFB** P2H **00** => **0FFC** P2L

*Application of the example from the manual "**MK14 MicroComputer Training Manual**" page 45*

1) Numbers entry :
go to address 0F20
then input first number xxxx in two parts
[Abort]0F20[Term]xx[MEM]xx[MEM]
then input second number yyyy in two parts
yy[MEM]yy[MEM]

2) Launch :
[Abort]0F24[GO]

3) Result :
[Abort]0F22[Term] display the first byte of the result
[MEM] display the second byte of the result

```
:0A0F20000000000002C223F221CA03
:090F2A0023C222F220CA223F007A
:020FFB000F00E5
:00000001FF
```

```
0f24          .OR      0xf24
       0020   Nbr1H    .EQ 0x020
       0021   Nbr1L    .EQ 0x021
       0022   Nbr2H    .EQ 0x022
       0023   Nbr2L    .EQ 0x023
0f24          ADD:
0f24 02       CCL
0f25 c2 23    LD Nbr2L(2)
0f27 f2 21    ADD Nbr1L(2)
0f29 ca 23    ST Nbr2L(2)
0f2b c2 22    LD Nbr2H(2)
0f2d f2 20    ADD Nbr1H(2)
0f2f ca 22    ST Nbr2H(2)
0f31 3f       XPPC 3
```